

FIG. 1

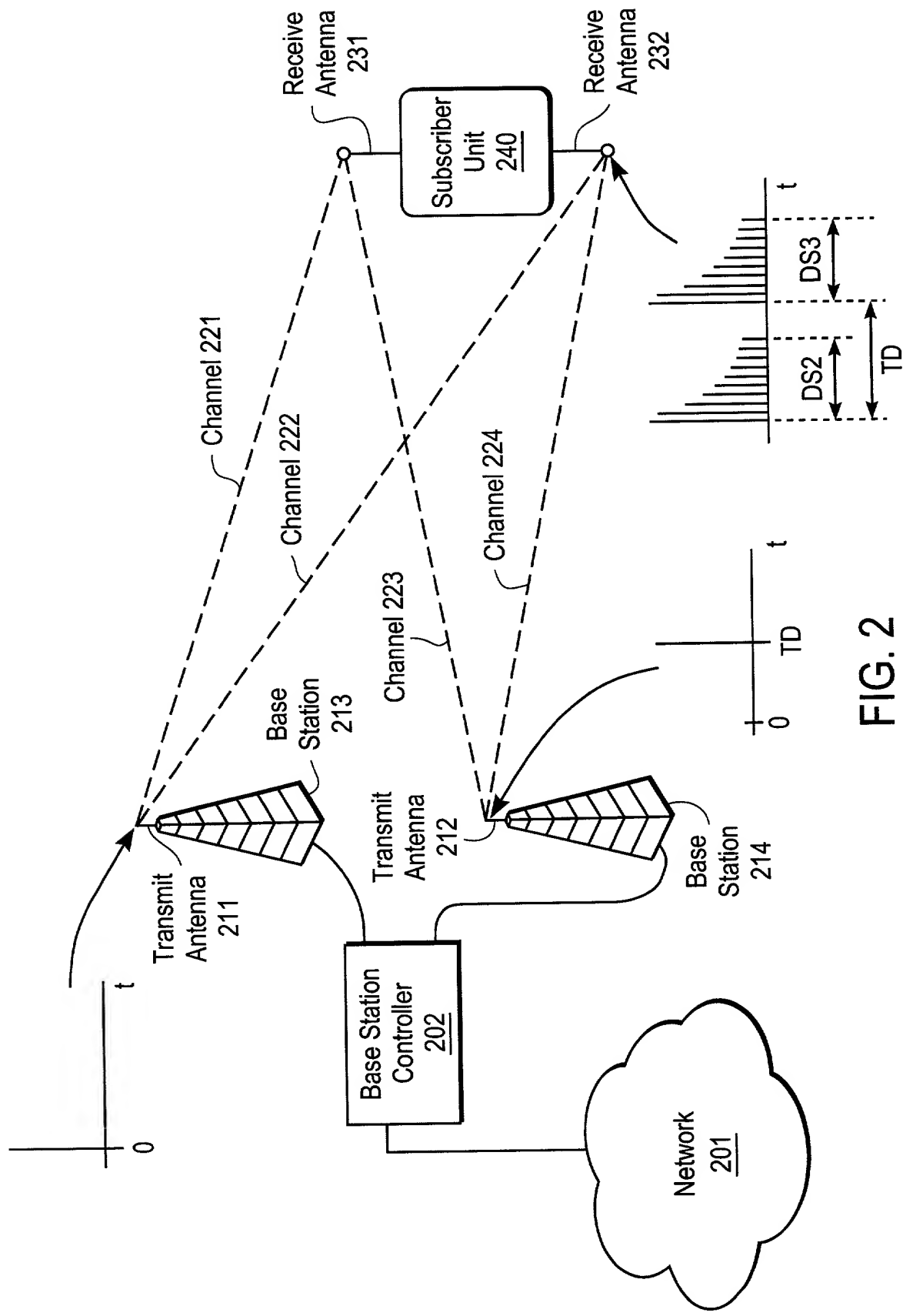


FIG. 2

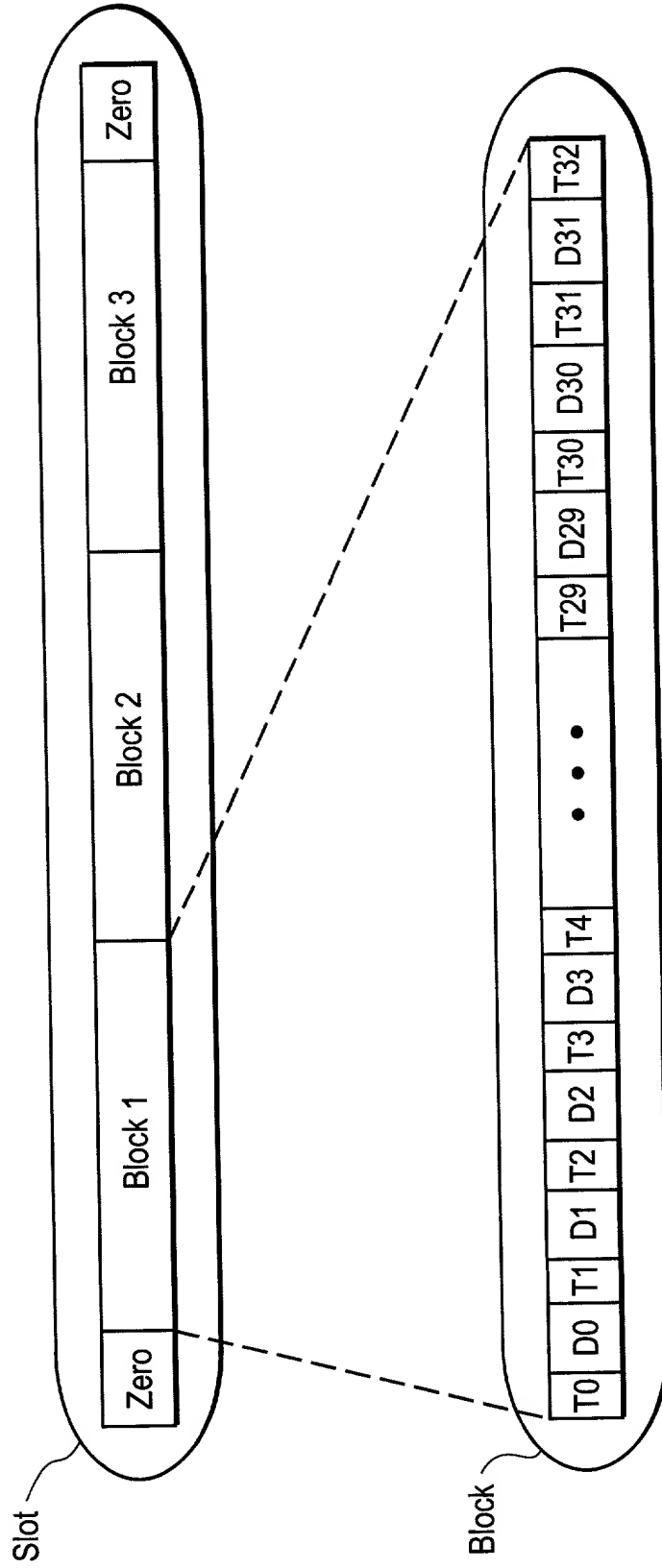


FIG. 3



FIG. 4 is a block diagram of an adaptive channel estimator system 400.

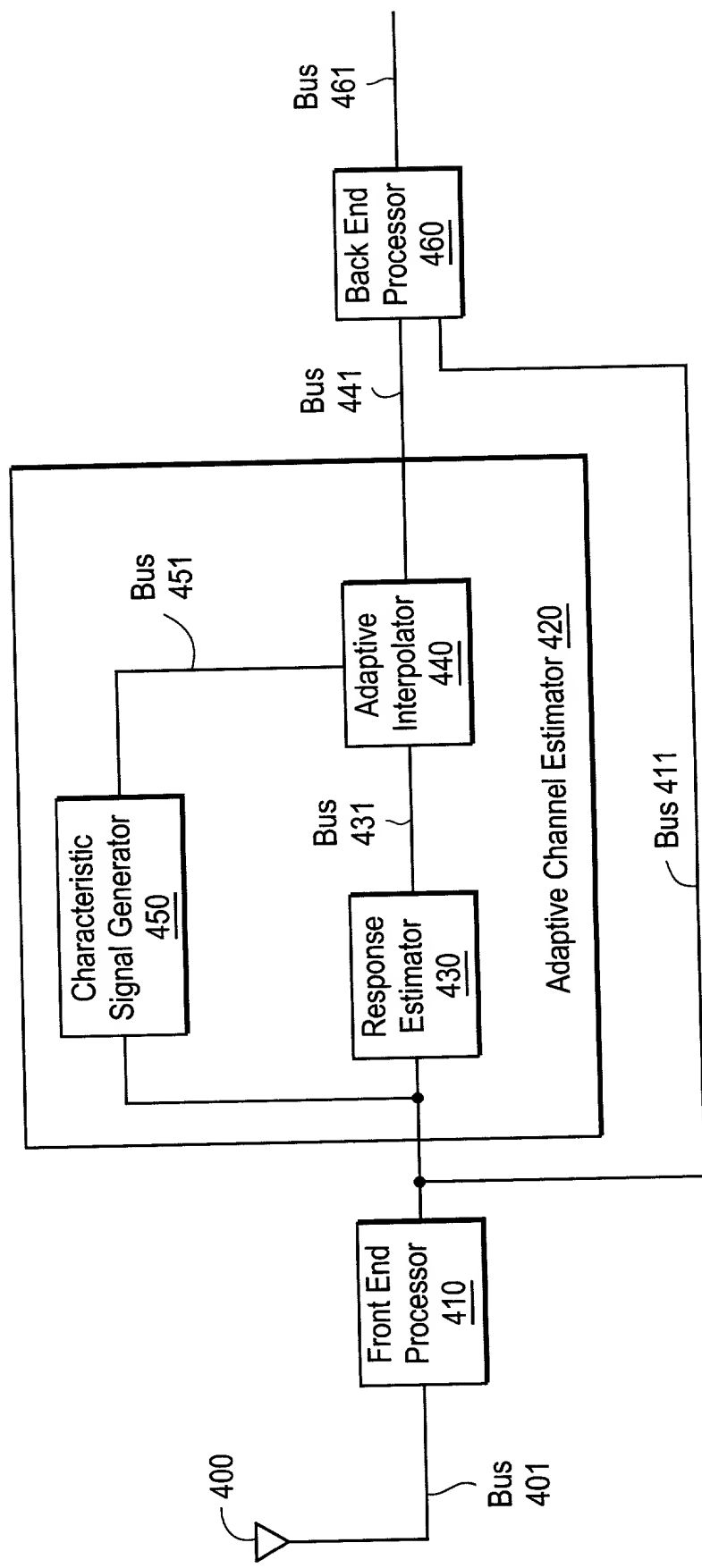


FIG. 4

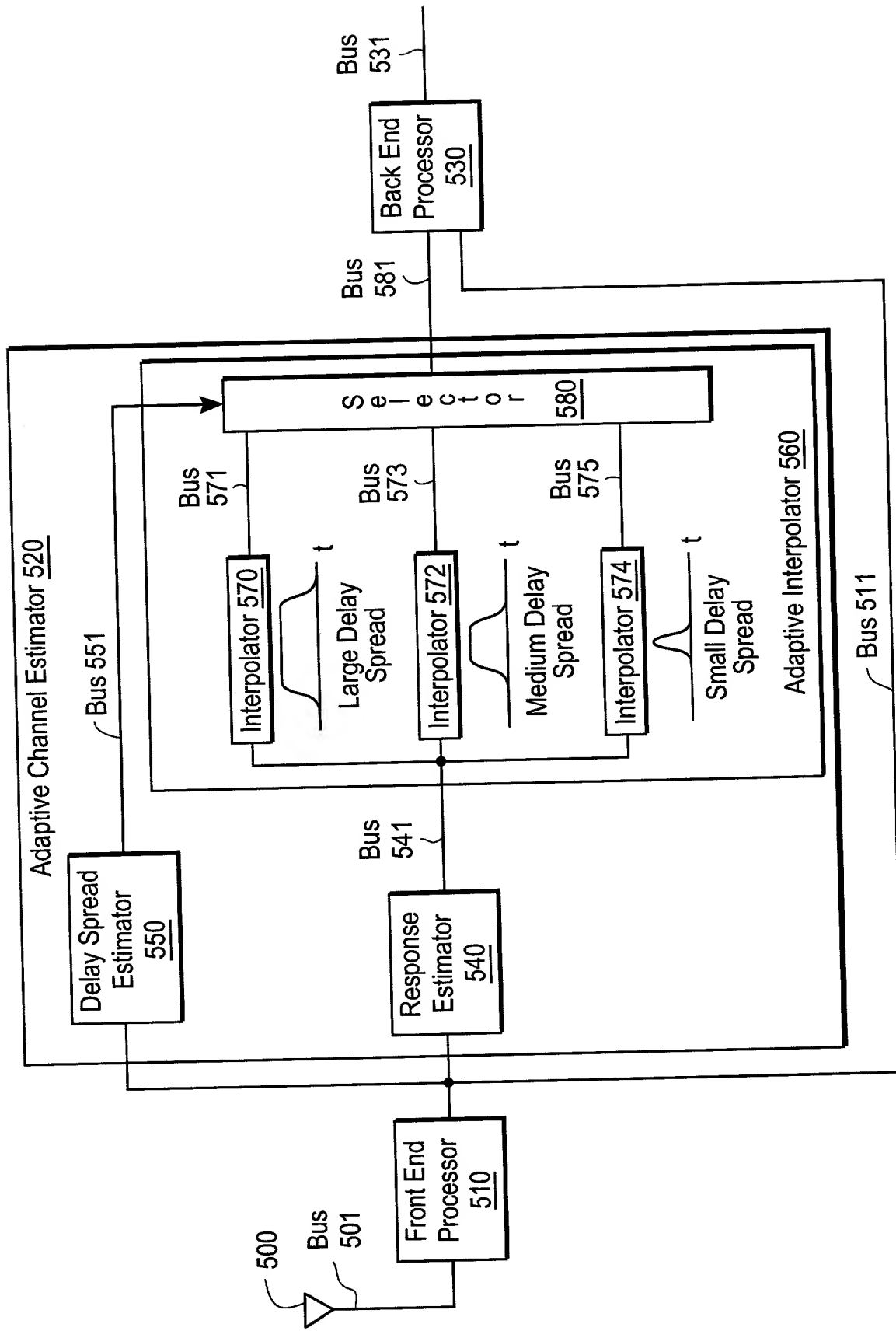


FIG. 5

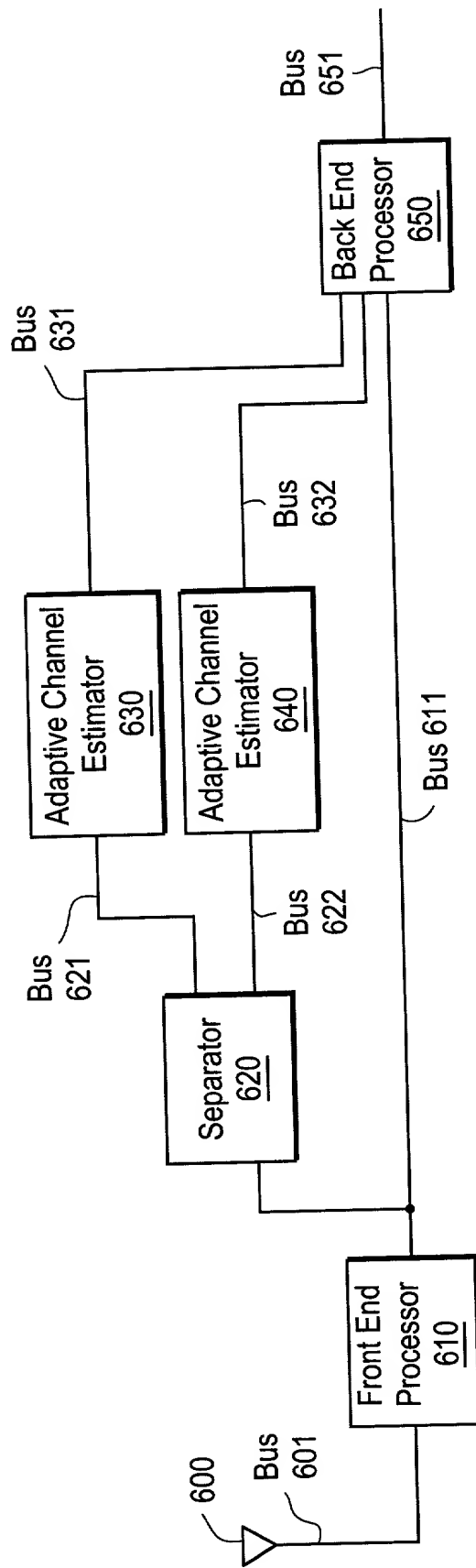


FIG. 6

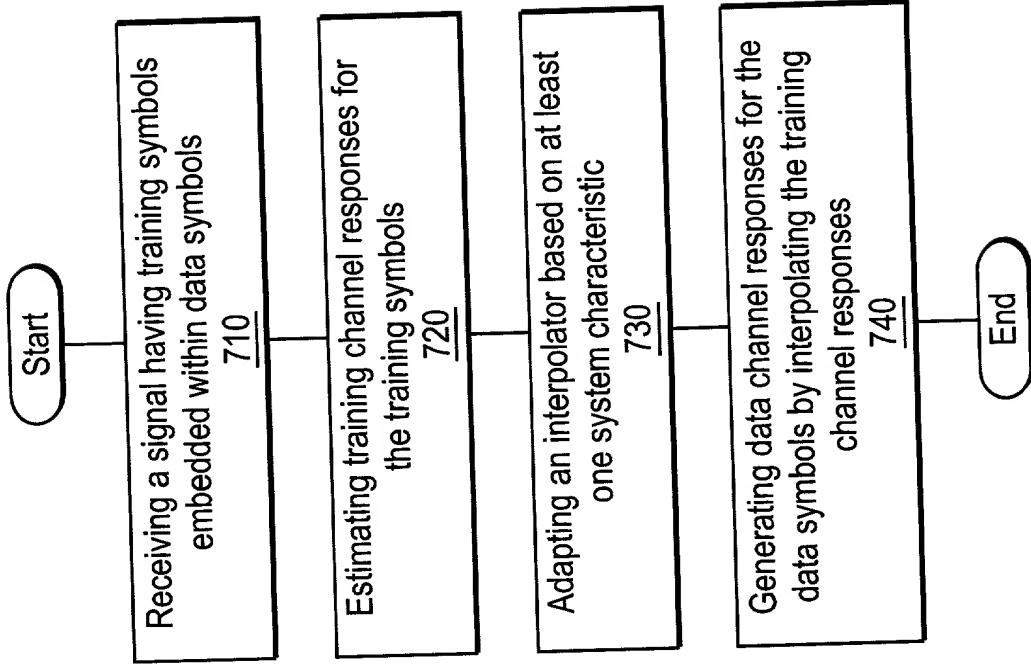


FIG. 7

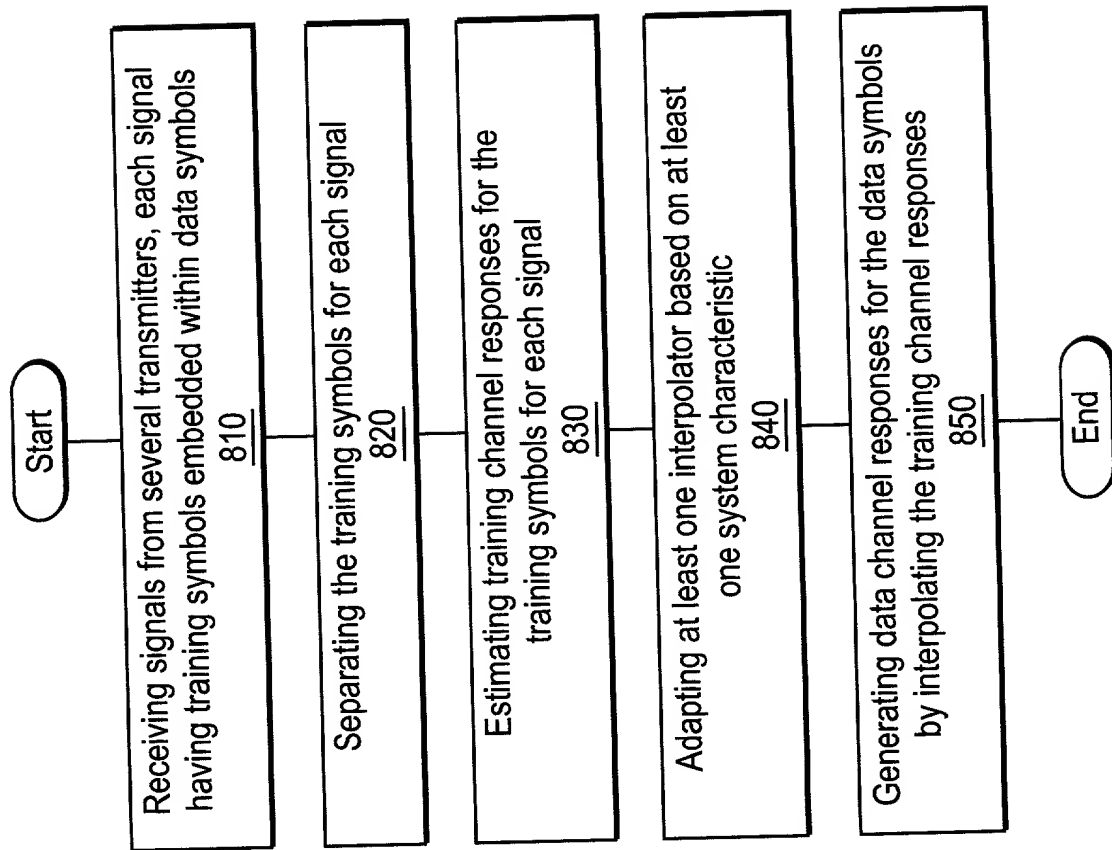


FIG. 8